

CAREER OPPORTUNITY

The Council for Scientific and Industrial Research (CSIR) is a leading scientific and technology research organisation, implementing projects throughout Africa and making a difference in people's lives.

Senior Electronic Engineer: Digital Signal Processing

About the job:

The CSIR has a vacancy for **Senior Electronic Engineer** in Sensor Science and Technology (SST) competence area within the Material Science and Manufacturing (MSM) unit. The incumbent will be required to work in a multi-disciplinary environment supporting research, design and development in the area of digital signal processing. This position is based in Pretoria.

About the area:

The Sensor Science and Technology (SST) competence area focuses on research and development in ultrasound transducers and systems, solid-state actuation and electro optic technologies and applications. Projects are conducted across the innovation chain in order to develop ultimate solutions into the market. Such work is often conducted in collaboration with industrial clients, both local and international.

Key responsibilities:

- Develop, test and implement software using scripting languages such as Matlab and Python.
- Develop and implement Labview software on hardware platforms, including FPGAs.
- Design, test and implement embedded hardware, firmware and software architecture;
- Perform system testing and integration;
- Clear documentation of all phases and outputs of the development;
- Inputs to project planning, costing, and proposal writing;
- Ensure that quality processes are followed.

Qualifications, skills and experience:

- A Bachelor's degree in electronic engineering with at least six years relevant experience. A Master's degree will be an advantage;
- Must have experience in product development and R&D processes;
- Must have programming experience in Matlab and/or other similar programming languages.

- Knowledge and experience in development and implementation of digital signal processing algorithm is required and experience in digital communication would be an advantage;
- Experience in system engineering will be an advantage;
- Programming experience in C/C++, Android, or Java will be an advantage;
- Knowledge of VHDL/Verilog will be an advantage.
- A good understanding of digital and analogue circuits, interfaces and standards;
- Technical leadership abilities and willingness to coach and mentor junior engineers and technicians;
- Ability to work in a multidisciplinary environment with clients, engineers, scientists, project managers and technicians.

Should you meet the above requirements, please email your CV to jobapplications@csir.co.za with your name and surname, position title and reference number in the subject line, **(eg. John Smith: Snr Electronics Engineer – Digital Signal Processing: Reference No: 307321)**

Closing date: 30 June 2017

PLEASE NOTE THAT FEEDBACK WILL BE GIVEN TO SHORTLISTED CANDIDATES ONLY.

For more info, please contact the CSIR Recruitment Centre on **012 841 4774** or email us at Recruitmentinfo@csir.co.za

*The CSIR is an equal opportunity employer. As such, it is committed to the Employment Equity Act of 1998. By applying for this position at the CSIR, the applicant understands, consents and agrees that the CSIR may solicit a credit and criminal report from a registered credit bureau and/or SAPS (in relation to positions that require trust and honesty and/or entail the handling of cash or finances) and may also verify the applicant's educational qualifications and employment history. **The CSIR reserves the right to remove the advertisement at any time before the stated closing date and it further reserves the right not to appoint if a suitable candidate is not identified.***